

POSSIBLE SCHEDULE FOR CEG AY2017 INTAKE

CEG AY2017/18 Poly Intake who are *exempted from CG1111*

Sem 1	Sem 2	Sem 3	Sem 4	May – Jul	Sem 5	Sem 6	
CS1010 Programming Methodology	CG1112 EPP2 (6 MCs)	CG2027 Transistor-level Digital Circuits (2 MCs)	CG2023 Signals & Systems	CP3200 Student Internship Programme OR EG3612 Vacation Internship Programme (2 MCs in lieu IA + UEM3)	CG3207 OR CS3230	CG4002 CEG Capstone Project (8 MCs)	
EE2026 Digital Design	CS1231 Discrete Structures	CG2028 Comp Org (2 MCs)	CG2271 Real-Time Operating Syst		Technical Elective Breadth		
MA1301 ^{1,2} Introductory Math	CS2040C Data Structures & Algorithms	CS2113 Software Engrg & OOP	EG2401A Engrg Profsm (2 MCs)		Technical Elective Depth	Technical Elective Depth	
PC1222 ¹ Fundamentals of Physics II	MA1511 Engrg Calculus (2 MCs)	MA1508E Linear Algebra for Engrg	EE4204 Computer Networks		Technical Elective Depth	GES1xxx	
ES1103* (UEM1) OR GER1000	MA1512 Diff Eqn for Engrg (2 MCs)	ST2334 Probability & Statistics	Technical Elective Breadth		GEH1xxx	UEM2 (2 MCs)	
	GEQ1000	GER1000 (if not read in sem 1) OR UEM1	GET1xxx			Technical Elective (2 MCs) (if did not do SIP/VIP)	UEM3 (if did not do SIP/VIP)
		CFG1010 (2 MCs)/UEM2					
20 MCs	22 MCs	22 MCs	22 MCs	6 or 0 MCs	20 or 22 MCs	18 or 22 MCs	
TOTAL (MINIMUM) GRADUATION REQUIREMENTS = 160 MCs[#]							

including Advanced Placement Credits (APCs) for CG1111, CS2101 and UEM (20 MCs)

Important:

¹ In lieu of IA (10 MCs), Poly students are required to take MA1301 (if not exempted), PC1222 and SIP/VIP/technical elective (TE) totalling 10 MCs.

² Poly students exempted from MA1301, will take MA1511 and MA1512 in place, AND will need to take additional technical elective (to make up the 4 MCs shortfall).

- The GE pillars (with the exception of GER1000 & GEQ1000) can be taken in any semester; the above serve as a guide.

* If not exempted.

POSSIBLE SCHEDULE FOR CEG AY2017 INTAKE

CEG AY2017/18 Poly Intake who are **NOT** exempted from both EPP modules

Sem 1	Sem 2	Sem 3	Sem 4	May – Jul	Sem 5	Sem 6
CG1111 EPP1 (6 MCs)	CG1112 EPP2 (6 MCs)	CG2271 Real-Time Operating Syst	CG2023 Signals & Systems	CP3200 Student Internship Programme OR EG3612 Vacation Internship Programme (2 MCs in lieu IA + UEM3)	CG3207 OR CS3230	CG4002 CEG Capstone Project (8 MCs)
CS1010 Programming Methodology	CS1231 Discrete Structures	CS2113 Software Engrg & OOP	CG2027 Transistor-level Digital Circuits (2 MCs)		EG2401A Engrg Profsm (2 MCs)	
MA1301 ^{1,2} Introductory Math	CS2040C Data Structures & Algorithms	MA1508E Linear Algebra for Engrg	CG2028 Comp Org (2 MCs)		Technical Elective Breadth	Technical Elective Depth
PC1222 ¹ Fundamentals of Physics II	EE2026 Digital Design	ST2334 Probability & Statistics	EE4204 Computer Networks		Technical Elective Depth	GES1xxx
ES1103* (UEM1) OR GER1000	MA1511 Engrg Calculus (2 MCs)	GER1000 (if not read in sem 1) OR GET1xxx	Technical Elective Breadth		Technical Elective Depth	UEM2 (2 MCs)
	MA1512 Diff Eqn for Engrg (2 MCs)	CFG1010 (2 MCs)/UEM2	UEM1 (if exempted ES1103)		Technical Elective (2 MCs) (if did not do SIP/VIP)	UEM3 (if did not do SIP/VIP)
			GEQ1000		GEH1xxx	
22 MCs	22 MCs	22 MCs	24 MCs	6 or 0 MCs	22 or 24 MCs	18 or 22 MCs
TOTAL (MINIMUM) GRADUATION REQUIREMENTS = 160 MCs[#]						

including Advanced Placement Credits (APCs) for CS2101 and UEM (20 MCs)

Important:

¹ In lieu of IA (10 MCs), Poly students are required to take MA1301 (if not exempted), PC1222 and SIP/VIP/technical elective (TE) totalling 10 MCs.

² Poly students exempted from MA1301, will take MA1511 and MA1512 in place, AND will need to take additional technical elective (to make up the 4 MCs shortfall).

- The GE pillars (with the exception of GER1000 & GEQ1000) can be taken in any semester; the above serve as a guide.

* If not exempted.