

POSSIBLE SCHEDULE FOR CEG AY2016 INTAKE STUDENTS

Possible Schedule for AY2016/17 **Common Engrg Intake streamed to CEG2 in AY2017/18** (with 6-months Industrial Attachment)

Sem 1, AY16/17	Sem 2	Sem 3, AY17/18	Sem 4	Sem 5	Sem 6	Sem 7	Sem 8
EG1111 EPP I (6 MCs) (map to UEM)	EG1112 EPP II (6 MCs) (map to CG1108 & CG1001)	CS1020 Data Structures & Algorithms I	CG2023 Signals & Systems	CG3002 Embedded Systems Design Proj	CP3880 ATAP OR EG3611 IA (12 MCs)	CG4001 B.Eng. Dissertation (12 MCs)	
GER1000 Quantitative Reasoning	ES1531 Critical Thinking & Writing	CS1231 Discrete Structures	CG2271 Real-Time Operating Syst	CG3207 Computer Architecture		Technical Elective Breadth	Technical Elective Depth
MA1505 Math I	MA1506 Math II	EE2020 Digital Fundamentals (5 MCs)	CS2101 Effective Comm for Computing Professionals	EE3204 Computer Comms Networks I		GEH1xxx	Technical Elective Depth
CS1010E Programming Methodology (map to CS1010)	PC1432 Physics IIE	EE2021 Devices & Circuits	CS2103T Software Engrg	ST2334 Probability & Statistics	EE3031 Inno & Enterprise I	UEM	UEM
ES1103*/UEM	GES1xxx	GEQ1000 Asking Questions	EE2024 Prog for Computer Interfaces (5 MCs)	GET1xxx	EG2401 Engrg Profsm.		
22 MCs	22 MCs	21 MCs	21 MCs	22 MCs	19 MCs	18 MCs	18 MCs
TOTAL (MINIMUM) GRADUATION REQUIREMENTS - 160 MCs							

^Students on Industrial Attachment (IA) are allowed to take up to two modules that are offered in the evenings (subject to approvals and availability). Depending on the preferred semester for IA, the modules for sem 5 & 6 may be mutually-swapped.

Important:

- The minimum 12 MCs of Technical Electives satisfying the CEG **Breadth/Depth** requirements can be taken at any semester upon satisfying the pre-requisites.
- Students are encouraged to take more Technical Elective(s) and at least one business/management module, to satisfy UEM.
- The GE pillars and Unrestricted Elective Modules (UEM) can be taken at any semester; the above serves as a guide.

* If not exempted