

POSSIBLE SCHEDULE FOR CEG AY2013 INTAKE STUDENTS

Possible Schedule for CEG AY2013/14 **Direct** Intake (**without IA**) - revisions dated 27 Dec 2013

Sem 1	Sem 2	Sem 3	Sem 4	Sem 5	Sem 6	Sem 7	Sem 8
CG1001 Intro to Comp Engrg	CG1108 Electrical Engineering	CS2101 Effective Comm for Computing Professionals	CG2023 Signals & Systems	CG3002 Embedded Systems Design Project	EE3031 Innovation & Enterprise I	CG4001 B.Eng. Dissertation	CG4001 B.Eng. Dissertation
CS1010 Programming Methodology	CS1020 Data Structures & Algorithms I	CS2103T Software Engrg	CG2271 Real-Time Operating Syst	CG3207 Computer Architecture	Breadth Elective	HR2002 Human Capital in Organizations	Depth Elective
CS1231 Discrete Structures	ES1531 Critical Thinking & Writing	EE2020 Digital Fundamentals	EE2024 Programming for Computer Interfaces	EE3204 Computer Comms Networks I	Breadth Elective	Depth Elective	Depth Elective
MA1505 Math I	MA1506 Math II	EE2021 Devices & Circuits	ST2334 Probability & Statistics	EG2401 Engrg Profsm	Breadth Elective	UEM 1	UEM 3
PC1432 Physics IIE	GEM 1	GEM 2	SS	ULR Breadth 1	ULR Breadth 2	UEM 2	
ES1102*							
18 MCs	20 MCs	21 MCs	21 MCs	21 MCs	20 MCs	21 MCs	18 MCs
TOTAL GRADUATION REQUIREMENTS = 160 MCs							

Important:

- Students are strongly encouraged to take at least one business/management module to satisfy ULR Breadth/UEM.
 - The minimum 24 MCs of **electives** satisfying the CEG **Breadth/Depth** requirements can be taken at any semester upon satisfying the pre-requisites.
 - The University Level Requirements (GEMs, SS, ULR Breadths) and Unrestricted Elective Module Requirements (UEM) can be taken at any semester, the above serve as a guide.
- * If not exempted

POSSIBLE SCHEDULE FOR CEG AY2013 INTAKE STUDENTS

Possible Schedule for CEG AY2013/14 **Direct Intake (with 6 months IA)** - revisions dated 27 Dec 2013

Sem 1	Sem 2	Sem 3	Sem 4	Sem 5	Sem 6	Sem 7	Sem 8
CG1001 Intro to Comp Engrg	CG1108 Electrical Engineering	CS2101 Effective Comm for Computing Professionals	CG2023 Signals & Systems	CG3002 Embedded Systems Design Project	EG3601 or CP3880	CG4001 B.Eng. Dissertation	CG4001 B.Eng. Dissertation
CS1010 Programming Methodology	CS1020 Data Structures & Algorithms I	CS2103T Software Engrg	CG2271 Real-Time Operating Syst	CG3207 Computer Architecture		HR2002 Human Capital in Organizations	Depth Elective
CS1231 Discrete Structures	ES1531 Critical Thinking & Writing	EE2020 Digital Fundamentals	EE2024 Programming for Computer Interfaces	EE3204 Computer Comms Networks I		Breadth Elective	Depth Elective
MA1505 Math I	MA1506 Math II	EE2021 Devices & Circuits	ST2334 Probability & Statistics	EG2401 Engrg Profsm	EE3031 Innovation & Enterprise I ^	Breadth Elective	Depth Elective
PC1432 Physics IIE	GEM 1	GEM 2	SS	ULR Breadth 1	^	Breadth Elective	ULR Breadth 2
ES1102*							
18 MCs	20 MCs	21 MCs	21 MCs	21 MCs	16 MCs	21 MCs	22 MCs
TOTAL GRADUATION REQUIREMENTS = 160 MCs							

^Students on Industrial Attachment (IA) are allowed to take up to two modules that are offered in the evenings (subject to approvals and availability)

Important:

- Students are strongly encouraged to take at least one business/management module to satisfy ULR Breadth/UEM.
- The minimum 24 MCs of **electives** satisfying the CEG **Breadth/Depth** requirements can be taken at any semester upon satisfying the pre-requisites.
- The University Level Requirements (GEMs, SS, ULR Breadths) and Unrestricted Elective Module Requirements (UEM) can be taken at any semester, the above serve as a guide.

* If not exempted