POSSIBLE SCHEDULE FOR CEG AY2012 INTAKE STUDENTS

Possible Schedule for CEG AY2012/13 Direct Intake (without IA)

Sem 1	Sem 2	Sem 3	Sem 4	Sem 5	Sem 6	Sem 7	Sem 8
MA1505 Math I	**MA1506 Math II**	CG2271 Real-Time Operating Syst	~~CG2007~~ EE2024 Prog for Comp Interfaces (5 MCs)	CG3207 Computer Architecture	~~**EE3001**~~ **EE3031 Inno &Enterprise I**	CG4001 FYP	CG4001 FYP
PC1432 Physics IIE	CG1108 Electrical Engineering	**EE2020 Digital Fundamentals (5 MCs)**	**EE2023 Signals & Systems**	CG3002 Embedded Systems Design Project	BREADTH ELECTIVE	**HR2002 Human Capital in Organizations**	DEPTH ELECTIVE
CG1101 Programming Methodology	CG1103 Data Structures & Algorithms I	**EE2021 Devices & Circuits**	**ST2334 Probability & Statistics**	**EE3204 Computer Comms Networks I**	BREADTH ELECTIVE	DEPTH ELECTIVE	DEPTH ELECTIVE
CS1231 Discrete Structures	CG1413 Effective Team Communication	**CS2103 Software Engrg**	**EE2031 Circuits & Systems Design Lab (3 MCs)**	**EG2401 Engrg Profsm**	BREADTH ELECTIVE	UEM 2	UEM 4
SS	ULR 1	ULR 2	GEM 1	~~UEM 1 (2 MCs)~~ GEM 2	UEM 1	UEM 3	
ES1102*			~~GEM 2~~				
20 MCs	**20 MCs**	**21 MCs**	**20 MCs**	**21 MCs**	**20 MCs**	**21 MCs**	**18 MCs**
TOTAL GRADUATION REQUIREMENTS = 161 MCs							

Important:

- Students are strongly encouraged to take at least one business/management module to satisfy ULR Breadth/UEM.
- The minimum 24 MCs of electives satisfying the CEG Breadth / Depth requirements can be taken at any semester upon satisfying the pre-requisites.
- The University Level Requirements (GEMs, SS, ULR Breadths) and Unrestricted Elective Module Requirements (UEM) can be taken at any semester, the above serve as a guide.

* If not exempted

POSSIBLE SCHEDULE FOR CEG AY2012 INTAKE STUDENTS

Possible Schedule for CEG AY2012/13 Direct Intake (with 6 months IA)

Sem 1	Sem 2	Sem 3	Sem 4	Sem 5	Sem 6	Sem 7	Sem 8
MA1505 Math I	**MA1506 Math II**	CG2271 Real-Time Operating Syst	~~CG2007~~ EE2024 Prog for Comp Interfaces (5 MCs)	CG3207 Computer Architecture	EG3601 IAP or CP3880 ATAP	CG4001 FYP	CG4001 FYP
PC1432 Physics IIE	CG1108 Electrical Engineering	**EE2020 Digital Fundamentals (5 MCs)**	**EE2023 Signals & Systems**	CG3002 Embedded Systems Design Project		**HR2002 Human Capital in Organizations**	DEPTH ELECTIVE
CG1101 Programming Methodology	CG1103 Data Structures & Algorithms I	**EE2021 Devices & Circuits**	**ST2334 Probability & Statistics**	**EE3204 Computer Comms Networks I**		BREADTH ELECTIVE	DEPTH ELECTIVE
CS1231 Discrete Structures	CG1413 Effective Team Communication	**CS2103 Software Engrg**	**EE2031 Circuits & Systems Design Lab (3 MCs)**	**EG2401 Engrg Profsm**	**~~EE3001~~ EE3031 Inno & Enterprise I**	BREADTH ELECTIVE	DEPTH ELECTIVE
SS	ULR 1	ULR 2	GEM 1	~~UEM 1 (2 MCs)~~ GEM 2	^	BREADTH ELECTIVE	UEM 1
ES1102*			~~GEM 2~~				
20 MCs	**20 MCs**	**21 MCs**	**20 MCs**	**21 MCs**	**16 MCs**	**21 MCs**	**22 MCs**
TOTAL GRADUATION REQUIREMENTS = 161 MCs							

^Students on Industrial Attachment (IA) are allowed to take up to two modules that are offered in the evenings (subject to approvals and availability).

Important:

- Students are strongly encouraged to take at least one business/management module to satisfy ULR Breadth/UEM.
- The minimum 24 MCs of electives satisfying the CEG Breadth / Depth requirements can be taken at any semester upon satisfying the pre-requisites.
- The University Level Requirements (GEMs, SS, ULR Breadths) and Unrestricted Elective Module Requirements (UEM) can be taken at any semester, the above serve as a guide.

* If not exempted