

POSSIBLE SCHEDULE FOR CEG AY2012 INTAKE STUDENTS

Possible Schedule for **CEG** AY2012/13 **Common** Intake (without IA)

Sem 1, AY12/13	Sem 2	Sem 3, AY13/14	Sem 4	Sem 5	Sem 6	Sem 7	Sem 8
MA1505 Math I	MA1506 Math II	EE2020 Digital Fundamentals (5 MCs)	EE2024 Prog for Computer Interfaces (5 MCs)	CG3207 Computer Architecture	EE3031 Inno & Enterprise I	CG4001 FYP	CG4001 FYP
PC1431 (ULR 1)	PC1432 Physics IIE	EE2021 Devices & Circuits	CG2271 RTOS	CG3002 Design Project	BREADTH ELECTIVE	HR2002 Human Capital in Org.	DEPTH ELECTIVE
MLE1101 (UEM 1)	CS1010E Programming Methodology (map to CG1101)	EE2023 Signals & Systems	CS2103 Software Engrg	EE3204 Computer Comms Networks I	BREADTH ELECTIVE	DEPTH ELECTIVE	DEPTH ELECTIVE
EG1109 (ULR 2)	EG1108 Electrical Engrg (map to CG1108)*	CS1231 Discrete Structures	ST2334 Probability & Statistics	EG2401 Engrg Profsm.	BREADTH ELECTIVE	UEM 2	UEM 4
ES1102^	ES1531 Critical Thinking & Writing (map to CG1413)	CS1020E CS1020 Data Structures & Algorithms I (map to CG1103)	EE2031 Circuits & Systems Design Lab (3 MCs)	GEM 1	GEM 2	UEM 3	UEM 5 (2-MCs)
			SS				
16 MCs	19 MCs	21 MCs	24 MCs	21 MCs	20 MCs	21 MCs	18 MCs
TOTAL GRADUATION REQUIREMENTS = 160 MCs							

*EG1108 = 3 MCs but CG1108 = 4 MCs. Earlier intakes are required to top up with 1-MC UEM but AY2012 common intake not affected, because EE2031 has been revised to 3 MCs (effective sem 1, AY13/14).

Important:

- Students are strongly encouraged to take at least one business/management module to satisfy ULR Breadth/UEM.
- The minimum 24 MCs of **electives** satisfying the CEG **Breadth** / **Depth** requirements can be taken at any semester upon satisfying the pre-requisites.
- The **University Level Requirements (GEMs/SS/Breadths)** and **Unrestricted Elective Module Requirements (UEM)** can be taken in any semester, the above only serves as a guide.

^ If not exempted